



Russian Supercomputing Days

Moscow, september 28-29

Russian Supercomputing Days is the first united international supercomputing conference carried on the best traditions of Russian and international supercomputing events.



Overview of Tianhe2 System and Application

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Outline

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- NUDT HPC Background
- Design and APP of Tianhe2 System
- Prospect of Tianhe-2A
- Summary



Overview

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~2,000 Teachers
~15,000 Students
... Others



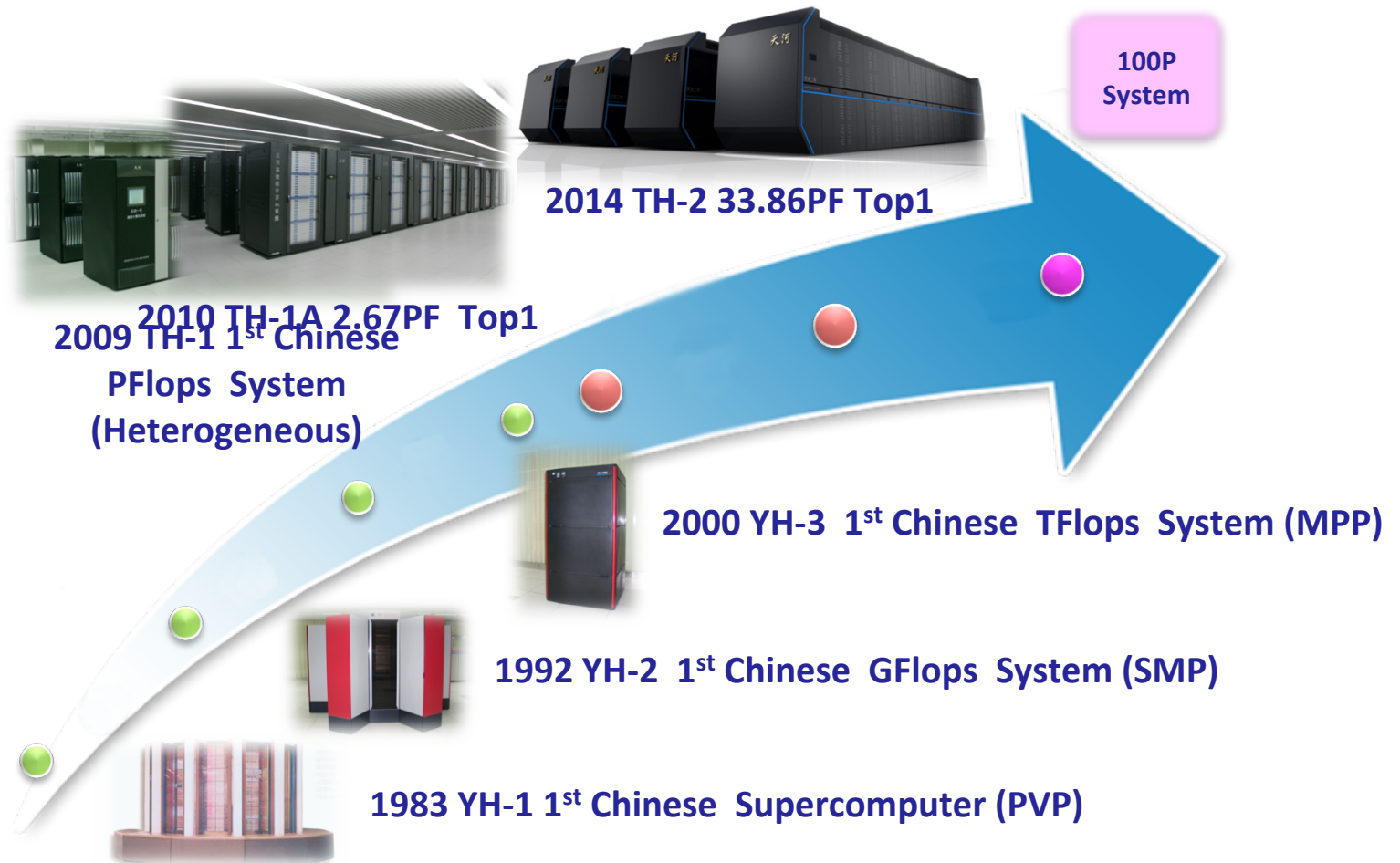
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Overview

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Supercomputers in NUDT, Changsha, China



Overview

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Supercomputing Centers in China



NSCC-Guangzhou,2013
Tianhe-2



NSCC-Wuxi
Shenwei-NG



NSCC-Changsha,2012
Tianhe-1A



NSCC-Jinan,2012
Shenwei-Bluelight



NSCC-Tianjin,2010
Tianhe-1A



NSCC-Shenzhen,2011
Dawning-6000



Overview

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Challenges(PSPRD)

- Performance
- Scalability
- Power consumption
- Reliability
- big Data

❑ Heterogeneous Trend

- Some of top-level supercomputers
- Tianhe-1/2, Titan ...

❑ Compute Efficiency

- More computations per transistor
- More computations per joule



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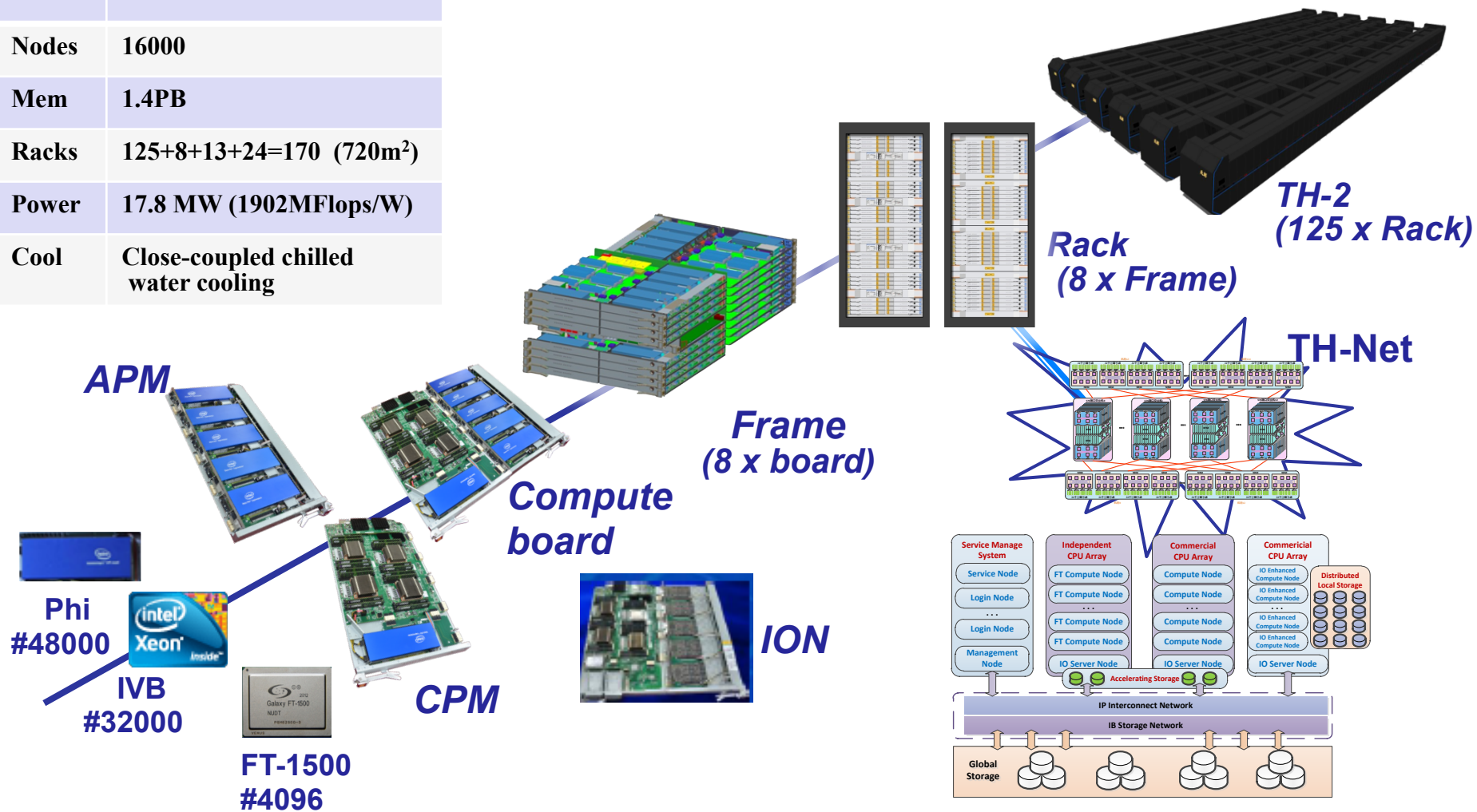
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Highlights of Tianhe-2

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Perf	54.9PFlops / 33.86PFlops
Nodes	16000
Mem	1.4PB
Racks	125+8+13+24=170 (720m ²)
Power	17.8 MW (1902MFlops/W)
Cool	Close-coupled chilled water cooling



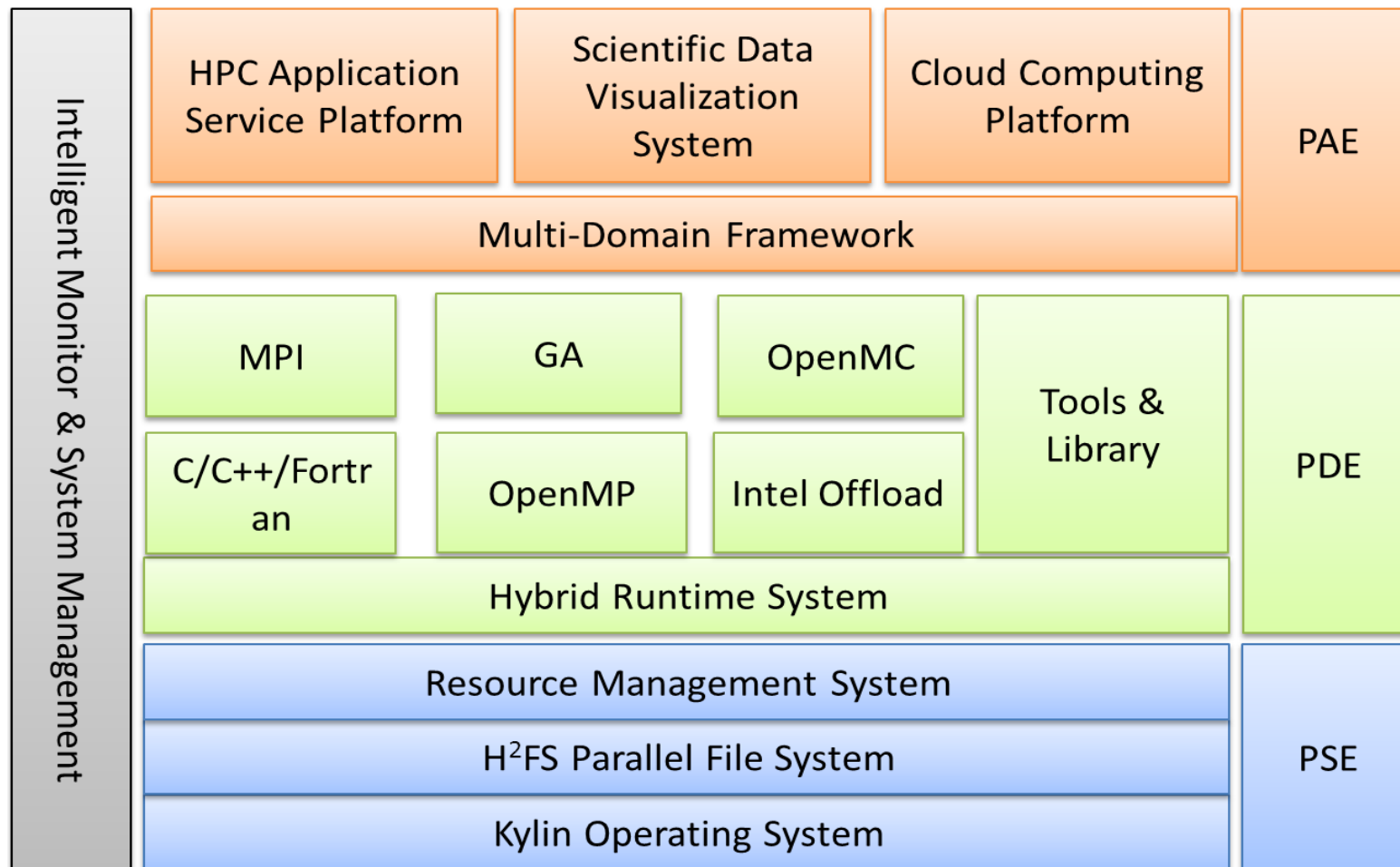
Hybrid Hierarchy shared storage System
12.4PB



Highlights of Tianhe-2

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Software Stack



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Adaptive Interconnect

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TH-Express 2+

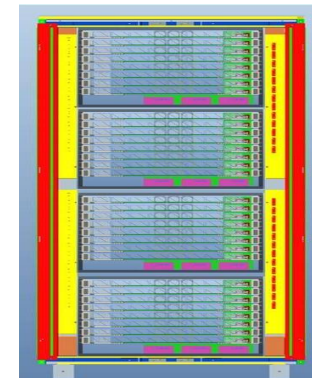
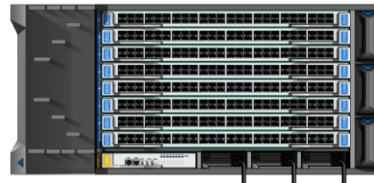
□ Network Interface Chip (NIC)

- Bandwidth
 - ◆ 14Gbps x 8lane
 - ◆ 12GB/s
- Function extension
 - ◆ Low latency and high rate operations
 - ◆ Collective comm offload
 - ◆ Parallel processing using Multiple DMA engines
 - ◆ Enhanced reliability

□ Network Router Chip (NRC)

- 24 ports
 - ◆ Up to 5.376 Tb/s of switching capacity
- Table-based routing
 - ◆ Multi-path adaptive routing
 - ◆ Oblivious routing

Extending to >100PFlops system



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Scalable MPI

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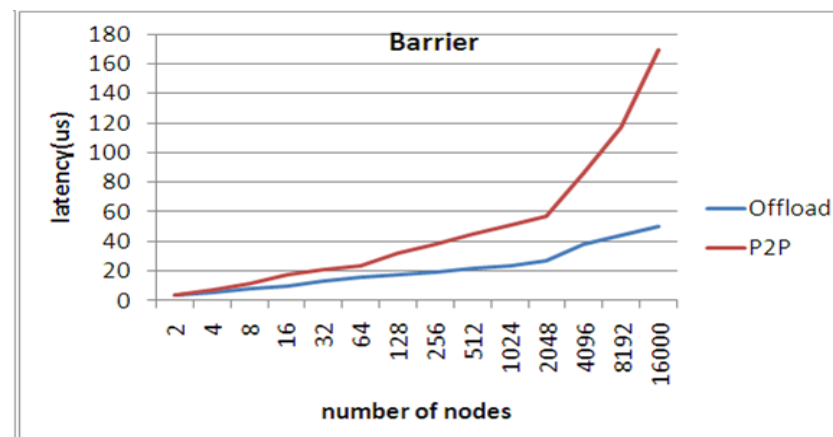
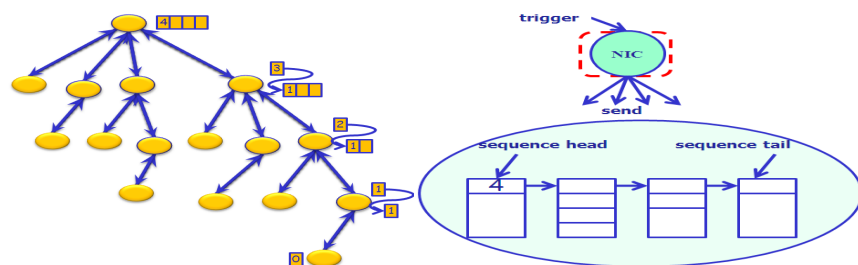
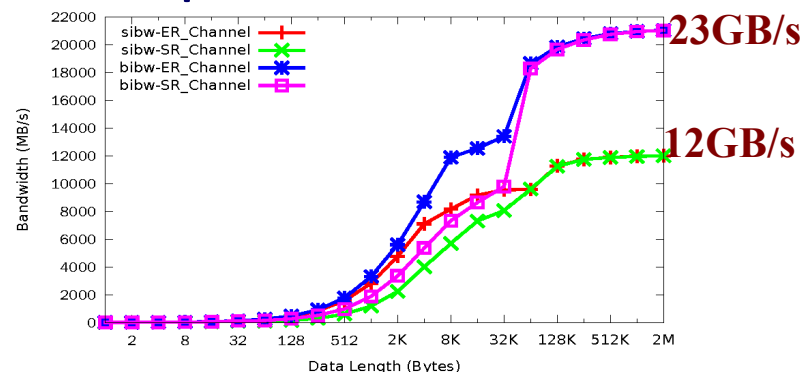
Message passing system based on TH-Express

Multiple Communication Protocols

- Performance oriented
- Scalability oriented
- Combine application model
- Zero-copy data transfer

Collective Operation Offload

- Construct topology-aware algorithm tree dynamically
- Message pass automatically based on the trigger of NIC
- Bypass effect of OS noise, Reduce Latency of large scale



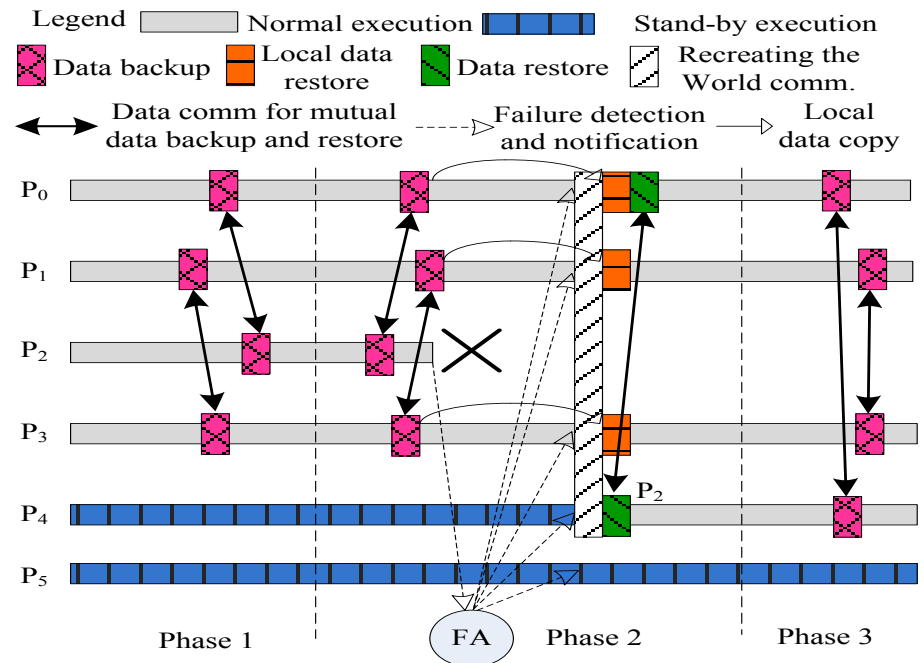
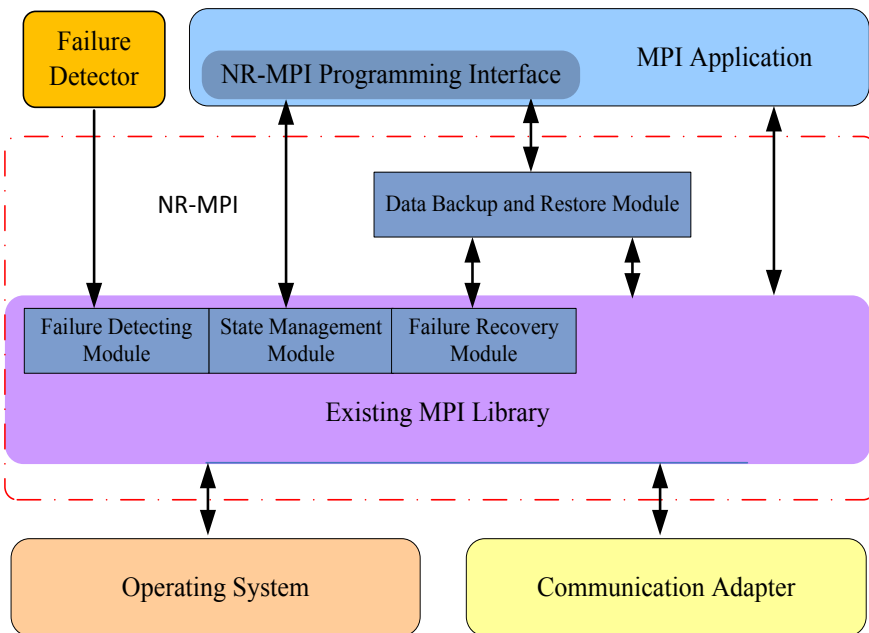
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Scalable MPI

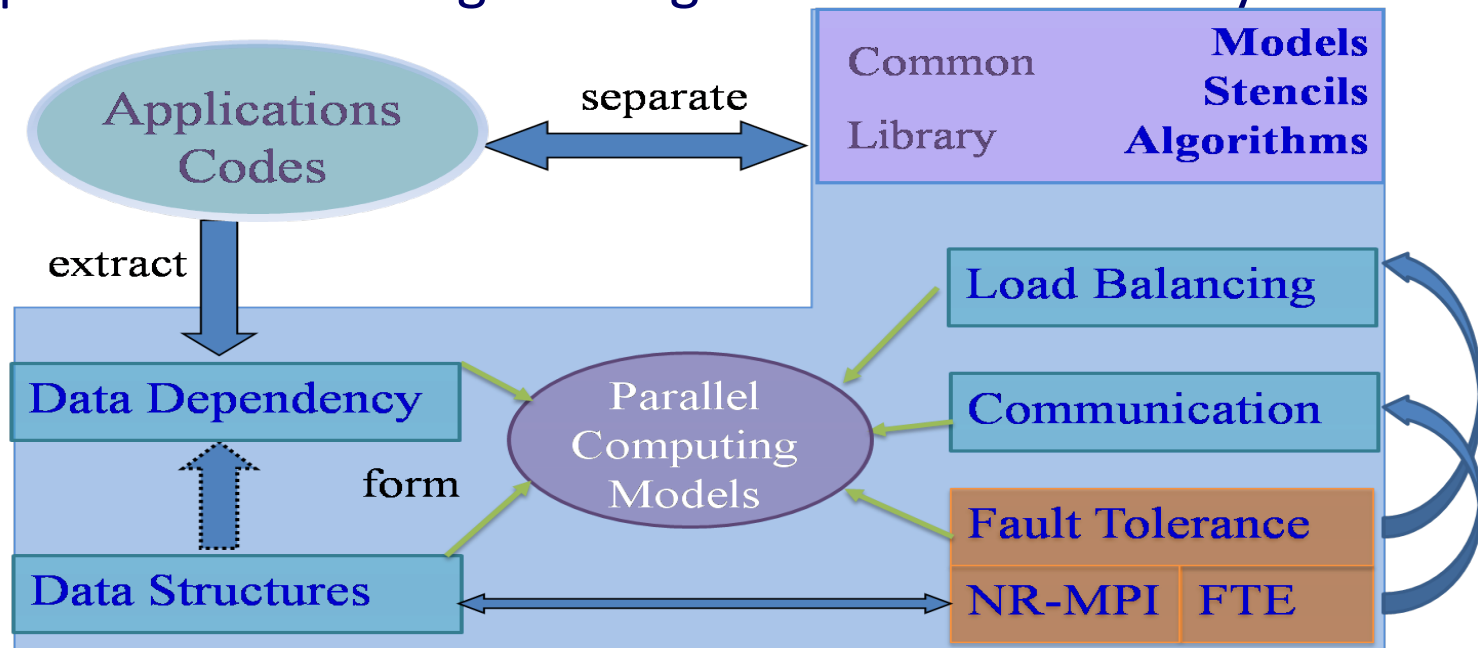
❑ Non-stop and fault Resilient MPI (NR-MPI)

- Application continue execution without being relaunched
- Failure detection and MPI state recovery done by runtime
- Data-backup by application-level diskless C/R
- Reconstruct of MPI communicator and channel



Domain Specific Framework

- ❑ Hide parallel programming complexity using millions of cores and the hierarchy of parallel computers
- ❑ Integrates the efficient implementations of parallel fast algorithms
- ❑ Provides efficient data structures and solver libraries
- ❑ Supports software engineering for code extensibility



Scalable IO Structure

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IO Architecture on Tianhe-2

Multiple Layers & Hybrid Storages

- ◆ Local Disk
- ◆ PCI-E SSD
- ◆ Disk Array

6400 local Disks

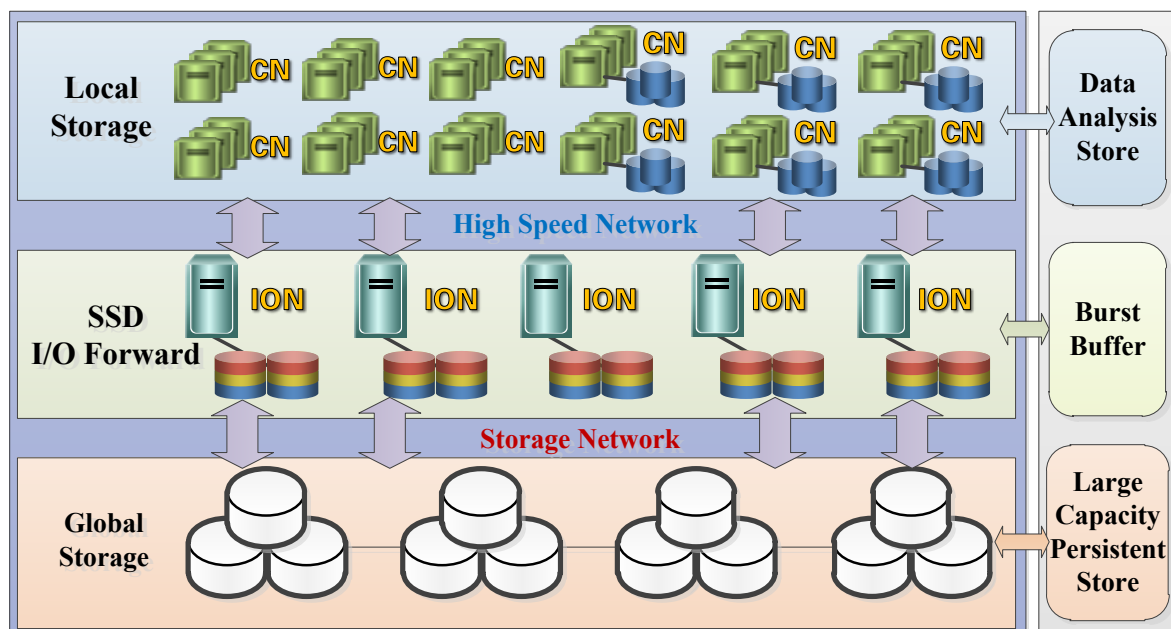
- ◆ Bus attached

256 IO nodes

- ◆ Burst: above 1TB/s
- ◆ TH-Express and IB QDR port

64 Storage Servers

- ◆ Sustained: about 100GB/s



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Scalable IO Structure

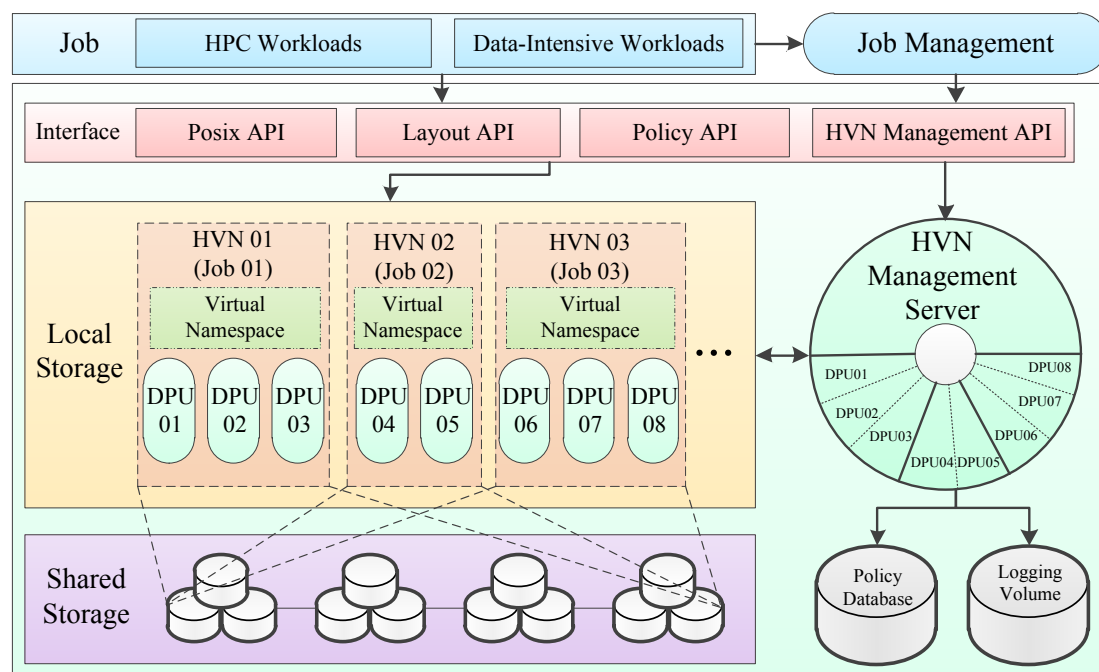
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□ H²FS: Hybrid Hierarchy File System

- DPU, A fundamental unit for data processing, tightly couples a compute node with its local storage
- HVN, Hybrid, Unified and Isolated dynamic namespace maintained by centralized servers
- Layered and enriched metadata, I/O hints as high level metadata

□ I/O API

- POSIX
- MPI-IO
- Extended API, layout and policy guide
- HDF5 over POSIX and extended API

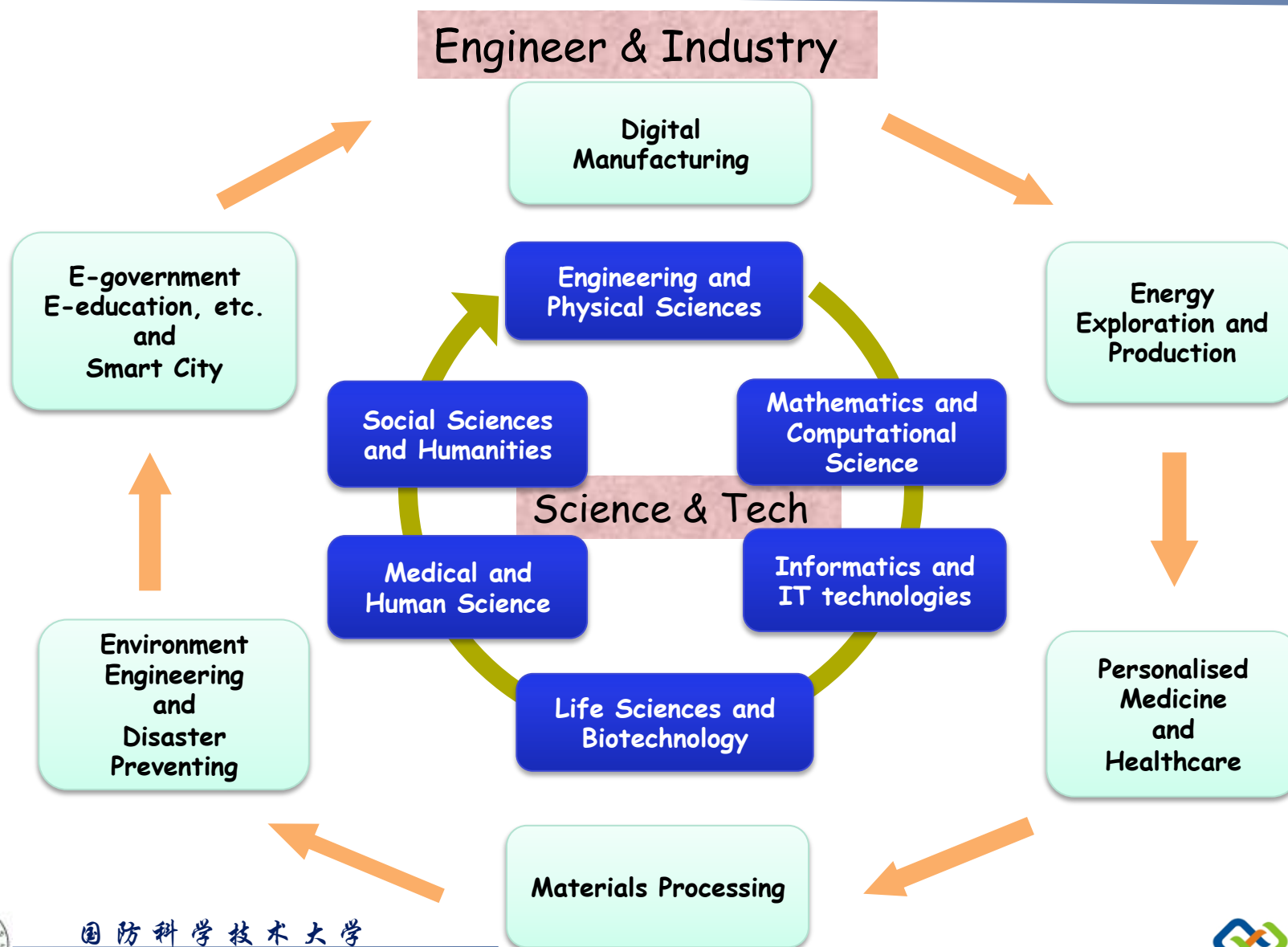


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Scalable Application

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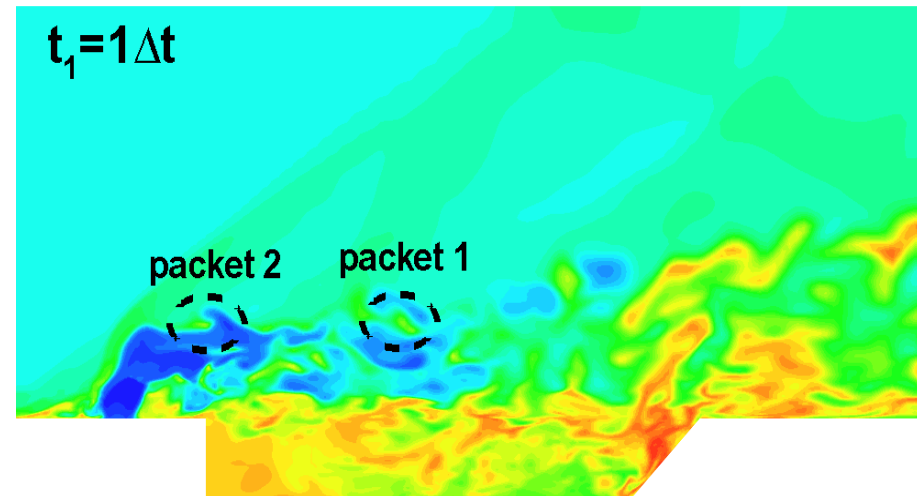
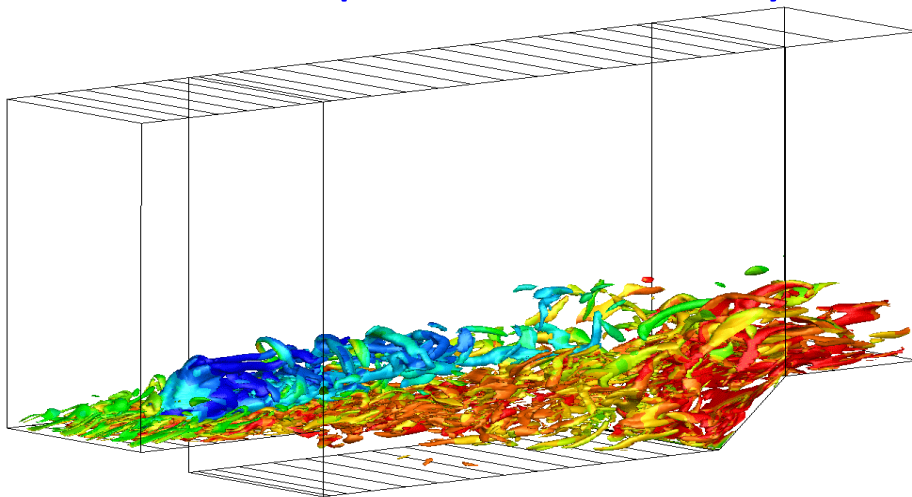
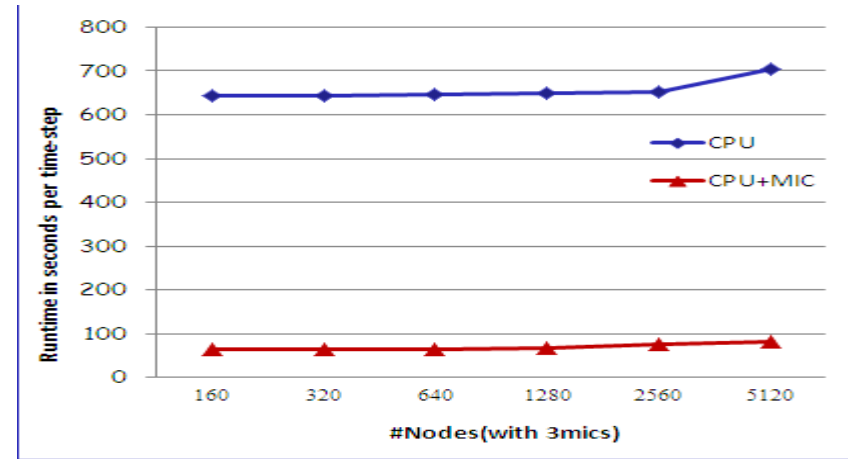


Application Case -- CFD

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□ Hybrid RANS/LES simulation of scramjet combustion

- CPU+MIC version is 8.63X to 13.86X faster than the original CPU version
- The largest scramjet combustion simulation up to now: totally 998,400 cores on 26,880 million cells, parallel efficiency 79%.



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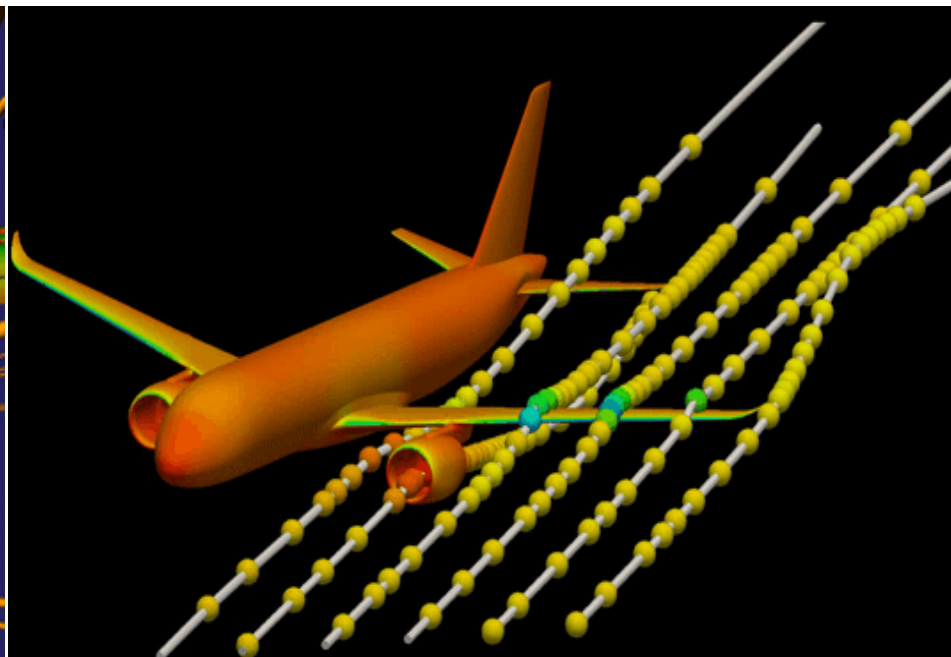
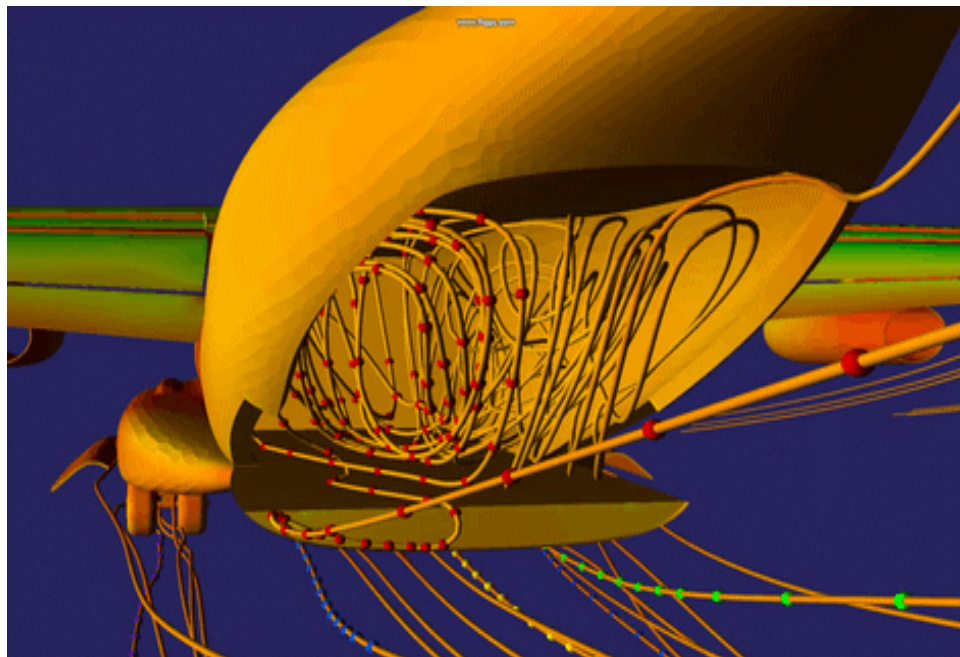


Application Case -- CFD

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❑ Large transport aircraft

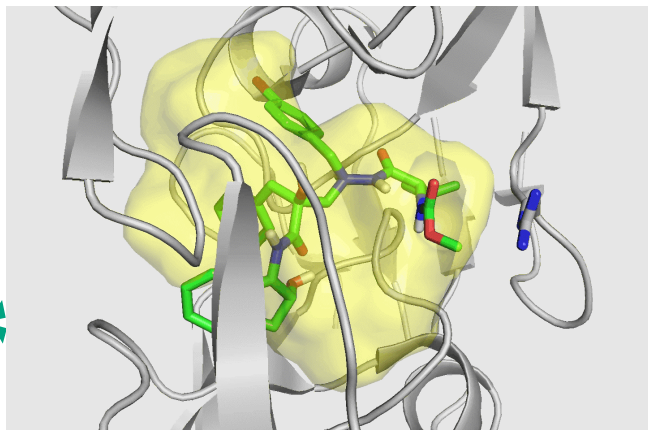
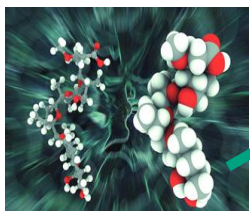
❑ Large passenger aircraft



Application Case – Bio-Medicine

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- ❑ High Throughput Virtual Screening
- ❑ Applications: Computer Aided Drug Design, Molecular Docking, and Virtual screening
- ❑ mD³DOCKxb, Lamarckian Genetic Algorithm
- ❑ Data Scale, 40 millions molecules, 800TB
- ❑ Bottleneck, IO BW, Comm BW



Find out the best 100 molecules;

Do experiment;

Clinical

Aim: Finish docking of all the drug molecules on earth within one day.



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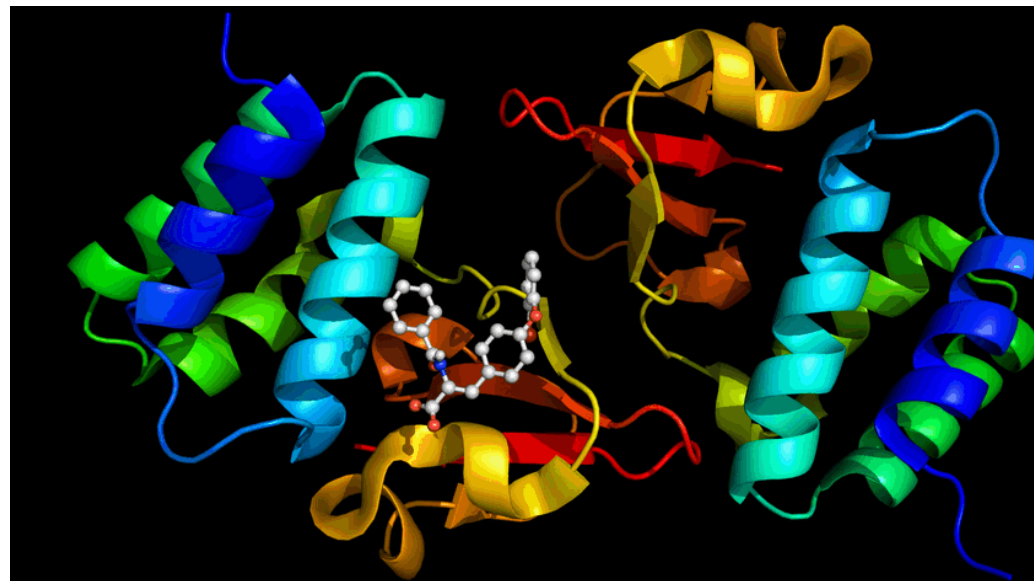
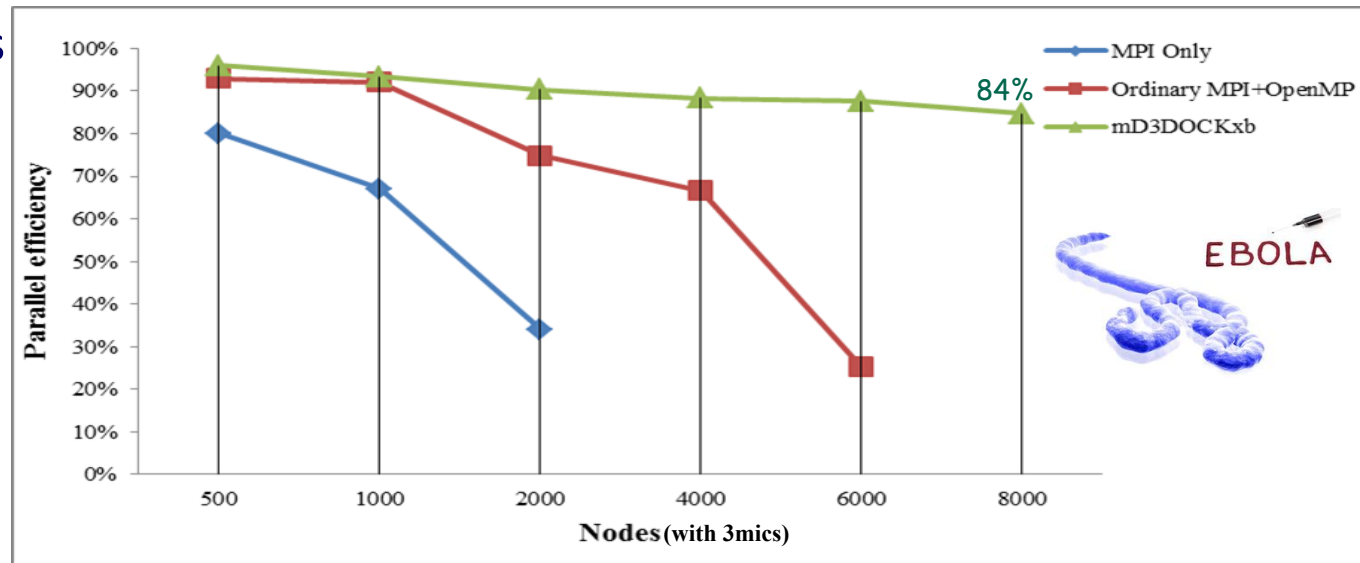
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Application Case – Bio-Medicine

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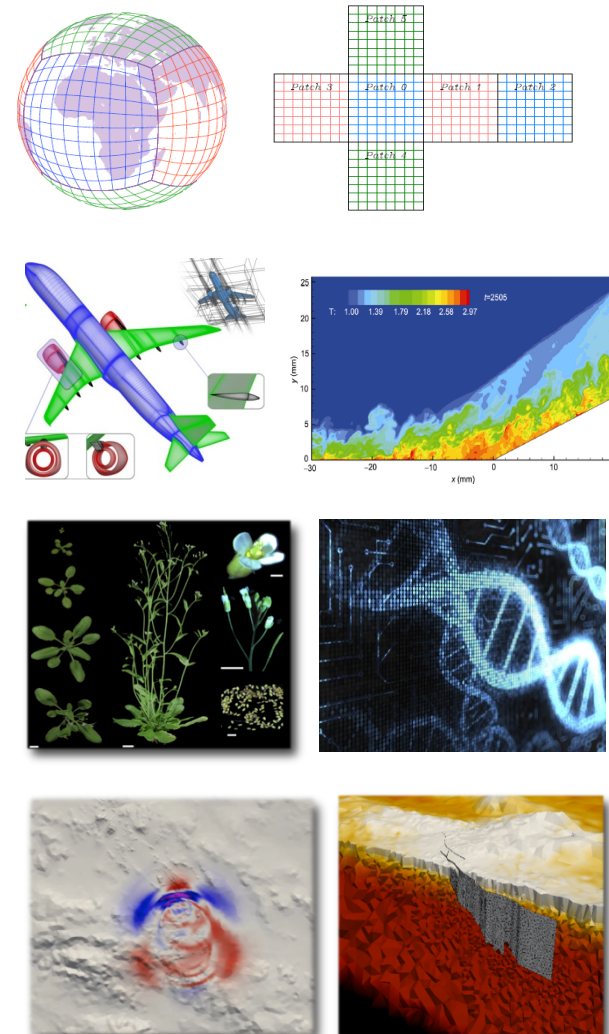
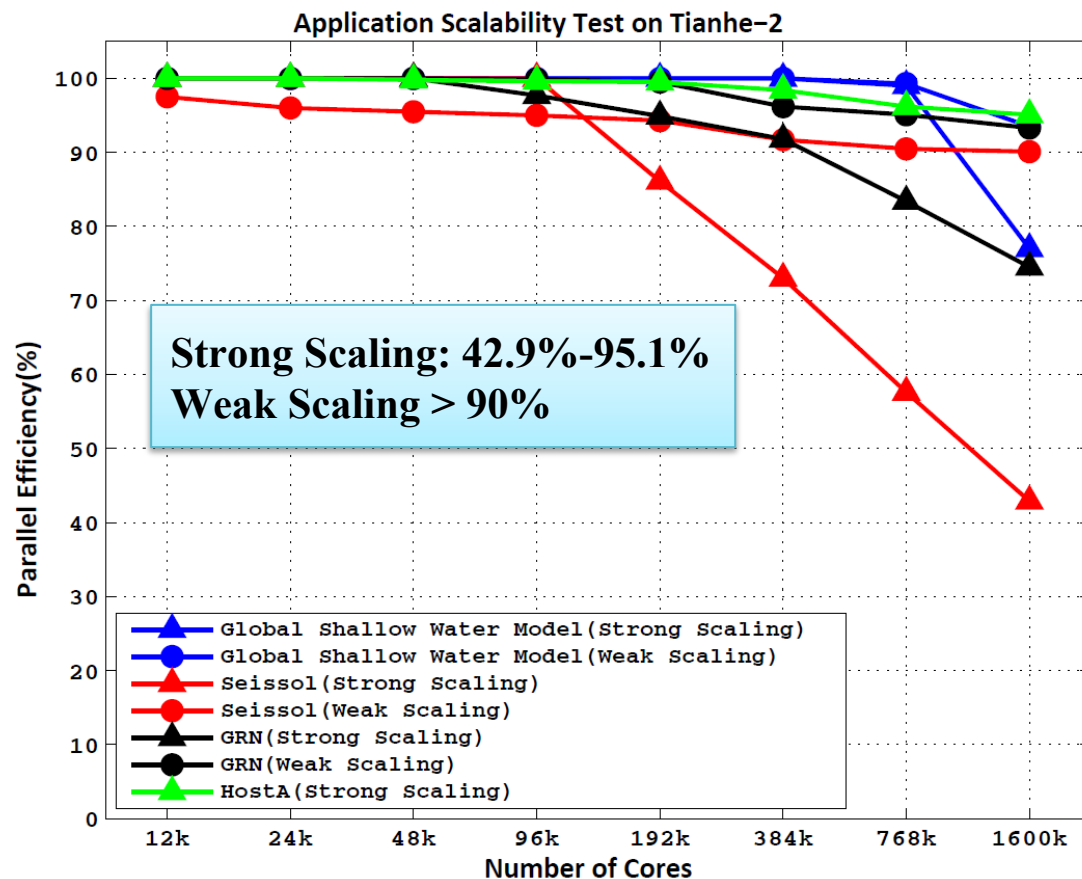
- Against Ebola virus
40 millions real
drug molecules
docking in 20hours
ON TH-2



Other Applications

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Other Applications

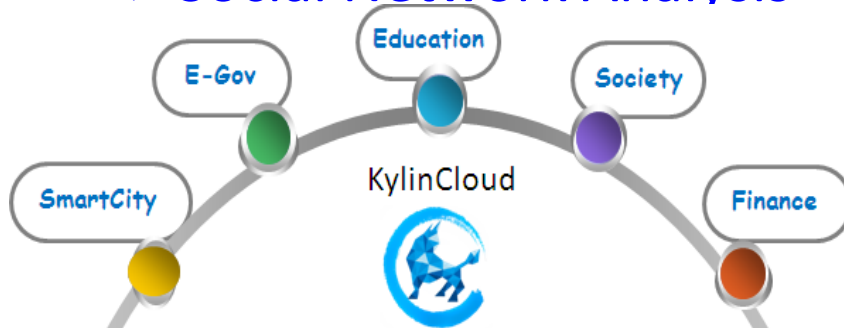
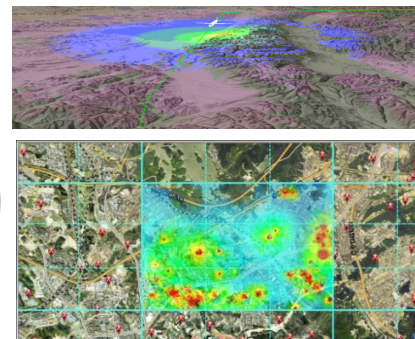
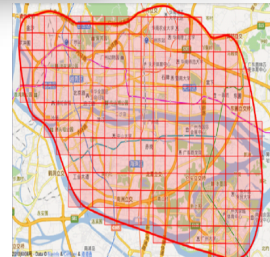
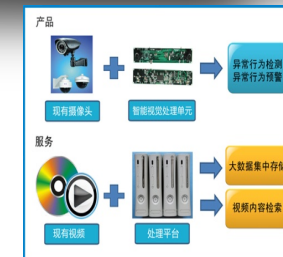


Cloud Computing & Bigdata

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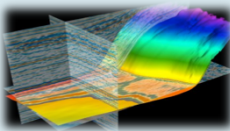


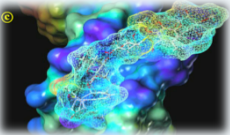
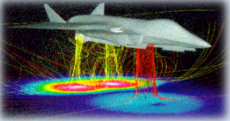


□ Additional Applications

- E-Gov
- micMR
- RenderCloud
- Health care
- Smart city
- Video Processing
- Education
- Social Network Analysis



Application scale in next 5 years

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Applications	Current Scale in China	Scale in next 5 years
 Seismic Exploration	2600km ² , 5km depth 217900 shots 2.2TB data	Millions of shots
 Genomics Research	2PB bioinformatics data	100PB bio data
 New Energy (Magnetic Confinement Fusion)	2 billion ions 0.83 billion electrons	100 billion electrons
 Drug Design	200-300ns Molecular Dynamics simulations	10 Million molecular 1000ns/day
 CFD (Aircraft Design)	3.5 billion mesh points	100 billion mesh points
 Universal Evolution (neutrinos)	110 billion particles	Trillion particles
 Smart City (Urban Electromagnetic Spectrum Monitoring System)	Area (Guangzhou city): 200km ² Grid size:1.0km*1.0km	Grid Size: 100m*100m



Status of Tianhe System

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System	Tianhe-1A	Tianhe-2	Tianhe-2A
System Peak(PF)	4.7	54.9	~100
Peak Power(MW)	4.04	17.8	~18
Total System Memory	262 TB	1.4 PB	~3PB
Node Performance(TF)	0.655	3.431	~6
Node processors	Xeon X5670 Nvidia M2050	Xeon E5 2692 Xeon Phi	Xeon or China CPU China Accelerator
System size(nodes)	7,168 nodes	16,000 nodes	~18,000
System Interconnect	TH Express-1	TH Express-2	TH Express-2+
File System	2 PB Lustre	12.4PB H ² FS +Lustre	~30PB H ² FS+TDM



China Accelerator

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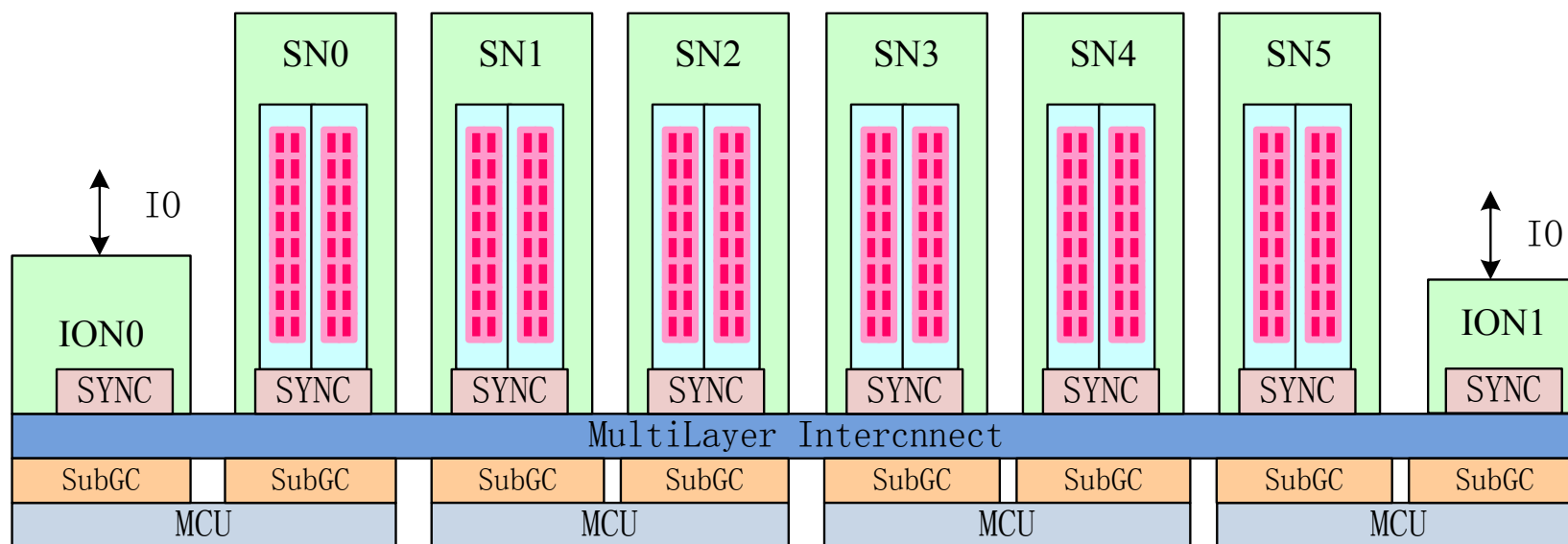
Matrix2000 GPDSP

High Performance

- 64bit Supported
- ~2.4/4.8TFlops(DP/SP)
- 1GHz, ~200W

High Throughput

- High-bandwidth Memory
- 32~64GB
- PCIE 3.0, 16x



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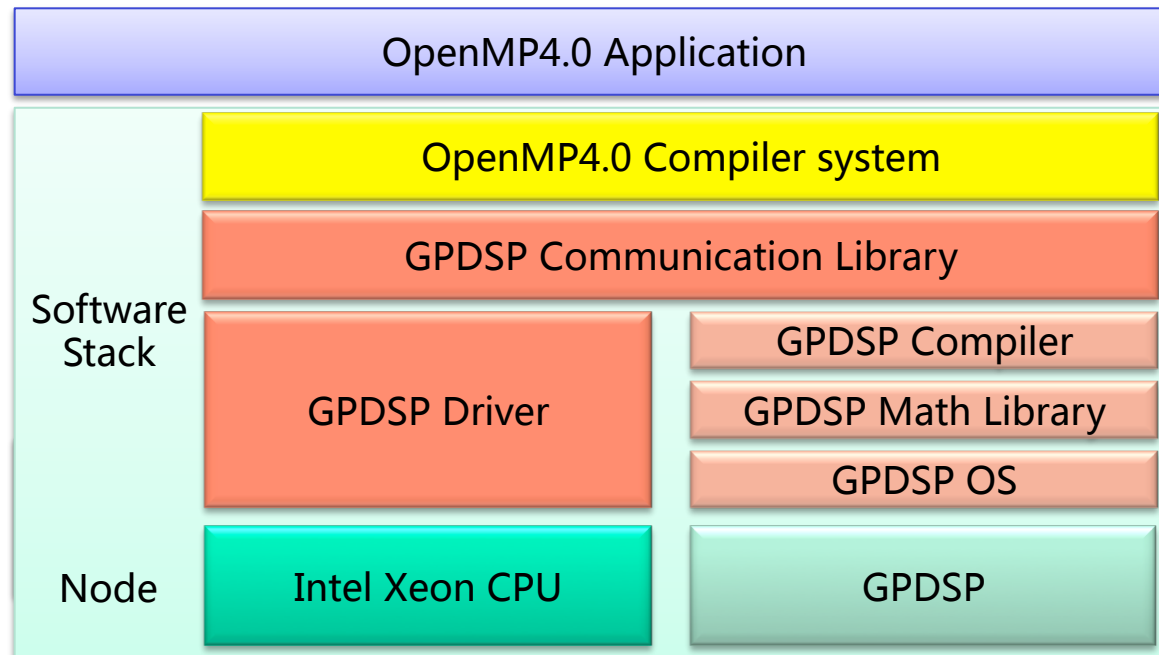


China Accelerator

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□ Software stack

- OpenMP4.0
- OS, Compiler and Math Library on GPDSP
- GPDSP Driver, Communication Library



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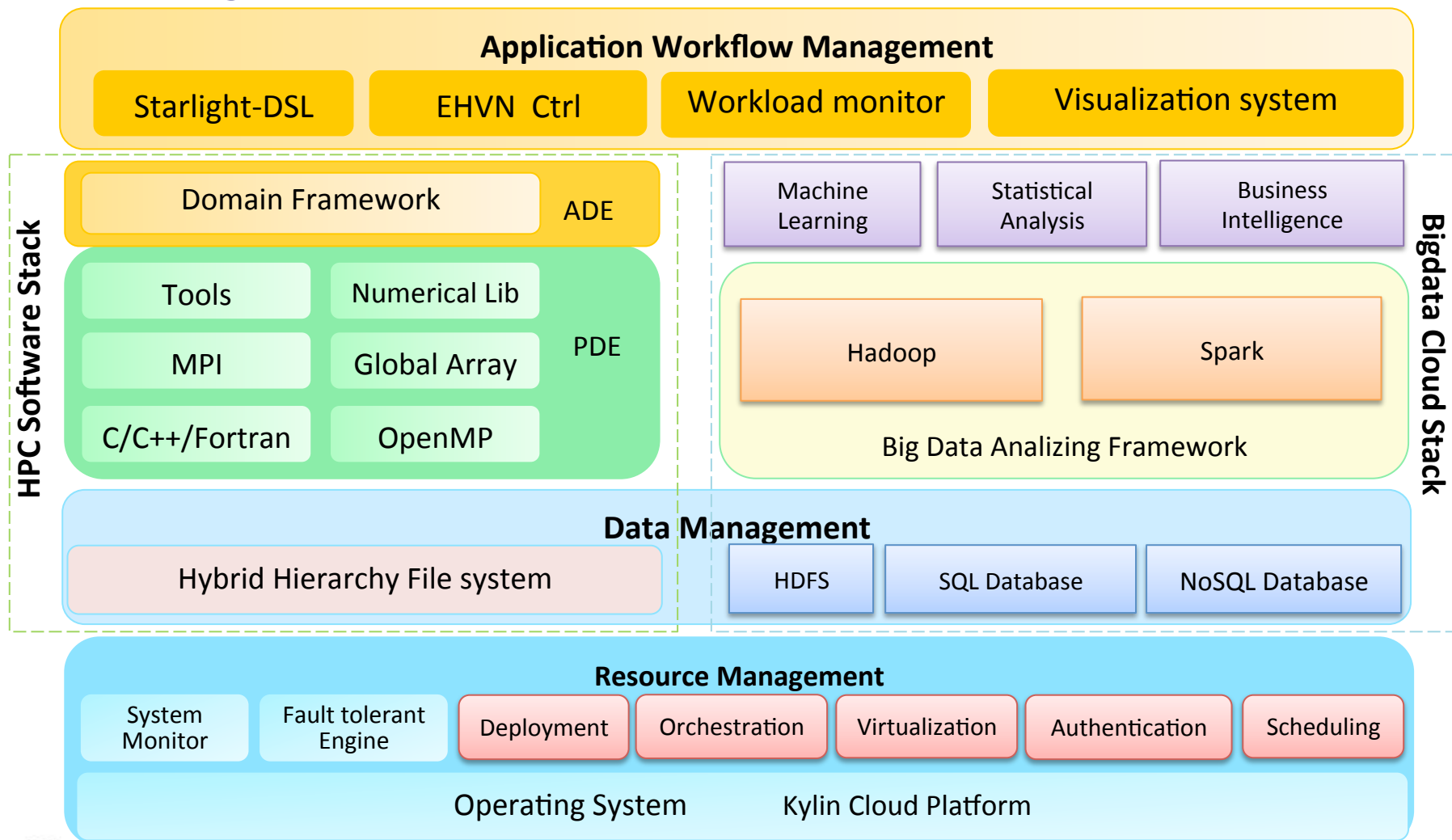
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HPC & Bigdata convergence Stack

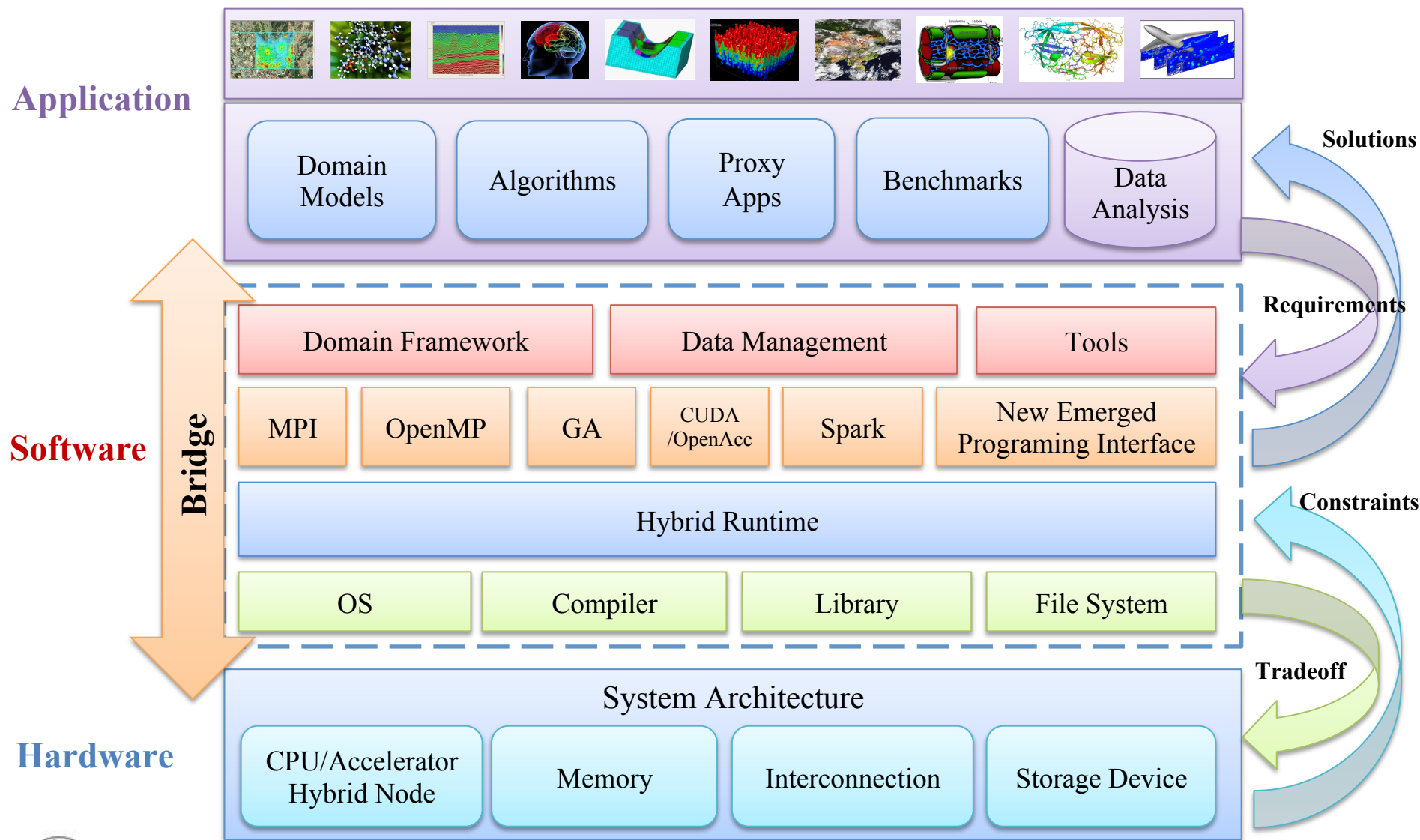
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□ Starlight Software Stack



Co-design Eco-system

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Summary

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- ❑ Heterogeneous Architecture
- ❑ Adaptive system and software design
- ❑ HPC & Bigdata Convergence
- ❑ Supercomputing Eco-system



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Thanks

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